LI80480C050HA9098-TR

5.0 inch, 800×480, IPS screen with wide viewing angle, air bonding RTP

Disclaimer: The product design is subject to alternation and improvement without prior notice.

Table of Contents

1 General Feature	
2 Mechanical Drawing	5
3 Input/Output Terminals	6
4 Electrical Characteristics	8
5 Timing Characteristics	
6 Optical Characteristics	
7 Environmental Reliability Test	15
8 Packing Capacity & Dimension	16
9 Appearance Inspection	17
10 Precautions for Use of LCD Modules	
11 Laminated Screen Introduction	

1 General Feature

1.1 LCD Parameters

	Feature	Description	Unit
	Size	5.0	inch
	Resolution	800(H)*480(V)	pixels
Display Spec.	Pixel Configuration	RGB stripe	E),
	Pixel Pitch	0.135(W)*0.135(H)	mm
	Viewing Direction	ALL	<u> </u>
	Outside Dimension	120.7(W)*75.8(H)*3.0(D)	mm
	Active Area	108.0(W)*64.8(H)	mm
Mechanical	Luminance	600	cd/m²
Characteristics	LED Numbers	20 LEDS	-
	Pin Order	From left to right 40PIN_0.5mm	-
	Weight		g
	Interface	RGB_24bit	-
Electrical	Color Depth	16.7M	colors
Characteristics	Driver Condition	3.3(Туре)	V
	LCM Driver IC	ST7262-G4-1-H	-
Temperature	Operating Temp.	-10~60	°C
Range	Storage Temp.	-20~70	°C

DWIN Frofessional, Creditable, Successful

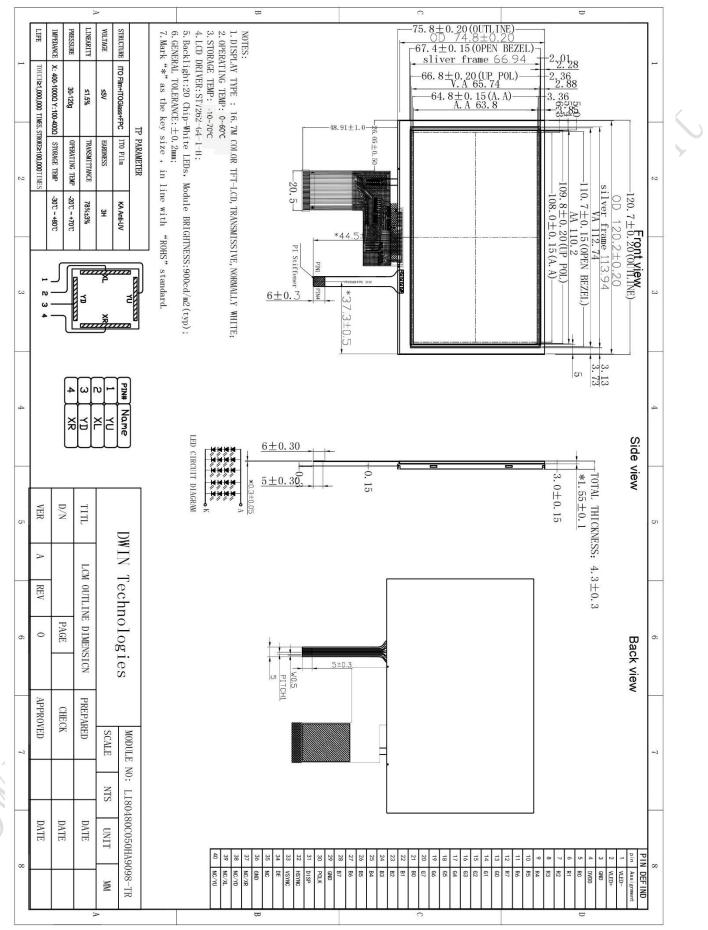
1.2 Touch Parameters

Feature	Description
Туре	RTP (Resistive touch panel)
Structure	ITO Film+ ITO Glass+ FPC
Outline Size(mm)	120.20(L)*74.80(W)*1.55(T)
Active Area(mm)	110.20(L)*63.80(W)
Surface Hardness	ЗН
Light Transmittance	78%±3%
Operating Temperature	-20~70℃
Storage Temperature	-30~80 ℃

Note: Requirements on Environmental Protection: RoHS

You can use dynamic screen saver wallpapers to avoid afterimages caused by fixed paper display for a long time

2 Mechanical Drawing



3 Input/Output Terminals

3.1 LCD Input/Output Terminals

1 VLED- Power for LED backlight cathode 2 VLED+ Power for LED backlight anode 3 GND Power ground 4 DVDD Power Voltage for digital circuit 3.3V 5 R0 Red data 7 R2 Red data 9 R4 Red data 9 R4 Red data 10 R5 Red data 11 R6 Red data 12 R7 Red data 13 G0 Green data 14 G1 Green data 15 G2 Green data 16 G3 Green data 17 G4 Green data 18 G5 Green data 19 G6 Green data 20 G7 Green data 21 B0 Blue data(LSB) 22 B1 Blue data 23 B2 Blue data 24 B3 Blue data<	Pin NO.	Symbol	Function	Remark
3 GND Power ground 4 DVDD Power Voltage for digital circuit 3.3V 5 R0 Red data(LSB) 6 R1 Red data 7 R2 Red data 9 R4 Red data 10 R5 Red data 11 R6 Red data 12 R7 Red data(LSB) 13 G0 Green data(LSB) 14 G1 Green data 15 G2 Green data 16 G3 Green data 17 G4 Green data 19 G6 Green data 19 G6 Green data 20 G7 Green data 21 B0 Blue data(LSB) 22 B1 Blue data 23 B2 Blue data 24 B3 Blue data 25 B4 Blue data 26 B5 Blue data	1	VLED-	Power for LED backlight cathode	
4 DVDD Power Voltage for digital circuit 3.3V 5 R0 Red data(LSB) 6 R1 Red data 7 R2 Red data 9 R4 Red data 9 R4 Red data 10 R5 Red data 11 R6 Red data 12 R7 Red data(SB) 13 G0 Green data(SB) 14 G1 Green data(SB) 14 G1 Green data 15 G2 Green data 16 G3 Green data 17 G4 Green data 18 G5 Green data 20 G7 Green data 21 B0 Blue data(LSB) 22 B1 Blue data 23 B2 Blue data 26 B5 Blue data 27 B6 Blue data 28 B7 Blue data <t< td=""><td>2</td><td>VLED+</td><td>Power for LED backlight anode</td><td></td></t<>	2	VLED+	Power for LED backlight anode	
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6 R1 Red data 7 R2 Red data 8 R3 Red data 9 R4 Red data 10 R5 Red data 11 R6 Red data 12 R7 Red data 13 G0 Green data 14 G1 Green data 15 G2 Green data 16 G3 Green data 17 G4 Green data 18 G5 Green data 20 G7 Qreen data(MSB) 21 B0 Blue data(LSB) 22 B1 Blue data 23 B2 Blue data 24 B3 Blue data 25 B4 Blue data 26 B5 Blue data 27 B6 Blue data 28 B7 Blue data 29 GND Power ground 30 PCLK	4	DVDD	Power Voltage for digital circuit 3.3V	
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9 R4 Red data 10 R5 Red data 11 R6 Red data 12 R7 Red data(NSB) 13 G0 Green data(LSB) 14 G1 Green data 15 G2 Green data 16 G3 Green data 17 G4 Green data 18 G5 Green data 20 G7 Green data 21 B0 Blue data(LSB) 22 B1 Blue data 23 B2 Blue data 24 B3 Blue data 25 B4 Blue data 26 B5 Blue data 27 B6 Blue data 28 B7 Blue data 29 GND Power ground 30 PCLK Pixel clock 31 DISP Standby mode, normally pull high STBYB="1" normal operation; STBYB="0", trining control, source driver will turn off, All output are high-Z <	7	R2	Red data	
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13 G0 Green data(LSB) 14 G1 Green data 15 G2 Green data 16 G3 Green data 17 G4 Green data 18 G5 Green data 19 G6 Green data 20 G7 Green data(MSB) 21 B0 Blue data(LSB) 22 B1 Blue data 23 B2 Blue data 24 B3 Blue data 25 B4 Blue data 26 B5 Blue data 27 B6 Blue data 28 B7 Blue data 29 GND Power ground 30 PCLK Pixel clock 31 DISP Standby mode, normally pull high STBYB= "1" normal operation; STBYB="0", timing control, source driver will turn off, All output are high-Z 32 HSYNC Horizontal sync input in parallel RGB. Negative polarity 33 VSYNC Vertical sync input in parallel RGB. Negative p	11	R6	Red data	
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15 G2 Green data 16 G3 Green data 17 G4 Green data 18 G5 Green data 19 G6 Green data 20 G7 Green data 21 B0 Blue data(LSB) 22 B1 Blue data 23 B2 Blue data 24 B3 Blue data 25 B4 Blue data 26 B5 Blue data 27 B6 Blue data 28 B7 Blue data 29 GND Power ground 30 PCLK Pixel clock 31 DISP Standby mode, normally pull high STBYB="1" normal operation; STBYB="0", timing control, source driver will turn off, All output are high-Z 32 HSYNC Horizontal sync input in parallel RGB. Negative polarity 33 VSYNC Vertical sync input in parallel RGB. Negative polarity 34 DEN Input data enable control. When DE mode, active High to enable data input(normally pull low)	13	G0	Green data(LSB)	
16G3Green data17G4Green data18G5Green data19G6Green data20G7Green data(MSB)21B0Blue data(LSB)22B1Blue data23B2Blue data24B3Blue data25B4Blue data26B5Blue data27B6Blue data28B7Blue data(MSB)29GNDPower ground30PCLKPixel clock31DISPStandby mode, normally pull high STBYB="1" normal operation; STBYB="0", timing control, source driver will turn off, All output are high-Z32HSYNCHorizontal sync input in parallel RGB. Negative polarity34DENInput data enable control. When DE mode, active High to enable data input(normally pull low)	14	G1	Green data	
17G4Green data18G5Green data19G6Green data20G7Green data(MSB)21B0Blue data(LSB)22B1Blue data23B2Blue data24B3Blue data25B4Blue data26B5Blue data27B6Blue data28B7Blue data(MSB)29GNDPower ground30PCLKPixel clock31DISPStandby mode, normally pull high STBYB="1" normal operation; STBYB="0", timing control, source driver will turn off, All output are high-Z32HSYNCHorizontal sync input in parallel RGB. Negative polarity34DENInput data enable control. When DE mode, active High to enable data input(normally pull low)	15	G2	Green data	
18G5Green data19G6Green data20G7Green data(MSB)21B0Blue data(LSB)22B1Blue data23B2Blue data24B3Blue data25B4Blue data26B5Blue data27B6Blue data28B7Blue data(MSB)29GNDPower ground30PCLKPixel clock31DISPStandby mode, normally pull high STBYB="1" normal operation; STBYB="0", timing control, source driver will turn off, All output are high-Z32HSYNCHorizontal sync input in parallel RGB. Negative polarity34DENInput data enable control. When DE mode, active High to enable data input(normally pull low)	16	G3	Green data	
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22B1Blue data23B2Blue data24B3Blue data25B4Blue data26B5Blue data27B6Blue data28B7Blue data(MSB)29GNDPower ground30PCLKPixel clock31DISPStandby mode, normally pull high STBYB= "1" normal operation; STBYB="0", timing control, source driver will turn off, All output are high-Z32HSYNCHorizontal sync input in parallel RGB. Negative polarity34DENInput data enable control. When DE mode, active High to enable data input(normally pull low)	20	G7	Green data(MSB)	
23B2Blue data24B3Blue data25B4Blue data26B5Blue data27B6Blue data28B7Blue data(MSB)29GNDPower ground30PCLKPixel clock31DISPStandby mode, normally pull high STBYB= "1" normal operation; STBYB="0", timing control, source driver will turn off, All output are high-Z32HSYNCHorizontal sync input in parallel RGB. Negative polarity34DENInput data enable control. When DE mode, active High to enable data input(normally pull low)	21	B0	Blue data(LSB)	
24B3Blue data25B4Blue data26B5Blue data27B6Blue data28B7Blue data(MSB)29GNDPower ground30PCLKPixel clock31DISPStandby mode, normally pull high STBYB= "1" normal operation; STBYB="0", timing control, source driver will turn off, All output are high-Z32HSYNCHorizontal sync input in parallel RGB. Negative polarity34DENInput data enable control. When DE mode, active High to enable data input(normally pull low)	22	B1	Blue data	
25B4Blue data26B5Blue data27B6Blue data28B7Blue data(MSB)29GNDPower ground30PCLKPixel clock31DISPStandby mode, normally pull high STBYB= "1" normal operation; STBYB="0", timing control, source driver will turn off, All output are high-Z32HSYNCHorizontal sync input in parallel RGB. Negative polarity34DENInput data enable control. When DE mode, active High to enable data input(normally pull low)	23	B2	Blue data	
26B5Blue data27B6Blue data28B7Blue data(MSB)29GNDPower ground30PCLKPixel clock31DISPStandby mode, normally pull high STBYB= "1" normal operation; STBYB="0", timing control, source driver will turn off, All output are high-Z32HSYNCHorizontal sync input in parallel RGB. Negative polarity34DENInput data enable control. When DE mode, active High to enable data input(normally pull low)	24	B3	Blue data	
27B6Blue data28B7Blue data(MSB)29GNDPower ground30PCLKPixel clock31DISPStandby mode, normally pull high STBYB= "1" normal operation; STBYB="0", timing control, source driver will turn off, All output are high-Z32HSYNCHorizontal sync input in parallel RGB. Negative polarity33VSYNCVertical sync input in parallel RGB. Negative polarity34DENInput data enable control. When DE mode, active High to enable data input(normally pull low)	25	B4	Blue data	
28B7Blue data(MSB)29GNDPower ground30PCLKPixel clock31DISPStandby mode, normally pull high STBYB= "1" normal operation; STBYB="0", timing control, source driver will turn off, All output are high-Z32HSYNCHorizontal sync input in parallel RGB. Negative polarity33VSYNCVertical sync input in parallel RGB. Negative polarity34DENInput data enable control. When DE mode, active High to enable data input(normally pull low)	26	B5	Blue data	
29GNDPower ground30PCLKPixel clock31DISPStandby mode, normally pull high STBYB= "1" normal operation; STBYB="0", timing control, source driver will turn off, All output are high-Z32HSYNCHorizontal sync input in parallel RGB. Negative polarity33VSYNCVertical sync input in parallel RGB. Negative polarity34DENInput data enable control. When DE mode, active High to enable data input(normally pull low)	27	B6	Blue data	
30 PCLK Pixel clock 31 DISP Standby mode, normally pull high STBYB= "1" normal operation; STBYB="0", timing control, source driver will turn off, All output are high-Z 32 HSYNC Horizontal sync input in parallel RGB. Negative polarity 33 VSYNC Vertical sync input in parallel RGB. Negative polarity 34 DEN Input data enable control. When DE mode, active High to enable data input(normally pull low)	28	B7	Blue data(MSB)	
31DISPStandby mode, normally pull high STBYB= "1" normal operation; STBYB="0", timing control, source driver will turn off, All output are high-Z32HSYNCHorizontal sync input in parallel RGB. Negative polarity33VSYNCVertical sync input in parallel RGB. Negative polarity34DENInput data enable control. When DE mode, active High to enable data input(normally pull low)	29	GND	Power ground	
31 DISP operation; STBYB="0", timing control, source driver will turn off, All output are high-Z 32 HSYNC Horizontal sync input in parallel RGB. Negative polarity 33 VSYNC Vertical sync input in parallel RGB. Negative polarity 34 DEN Input data enable control. When DE mode, active High to enable data input(normally pull low)	30	PCLK	Pixel clock	
All output are high-Z 32 HSYNC 33 VSYNC 33 VSYNC 34 DEN			Standby mode, normally pull high STBYB= "1" normal	
32 HSYNC Horizontal sync input in parallel RGB. Negative polarity 33 VSYNC Vertical sync input in parallel RGB. Negative polarity 34 DEN Input data enable control. When DE mode, active High to enable data input(normally pull low)	31	DISP	operation; STBYB="0", timing control, source driver will turn off,	
33 VSYNC Vertical sync input in parallel RGB. Negative polarity 34 DEN Input data enable control. When DE mode, active High to enable data input(normally pull low)			All output are high-Z	
34 DEN Input data enable control. When DE mode, active High to enable data input(normally pull low)	32	HSYNC	Horizontal sync input in parallel RGB. Negative polarity	
34 DEN enable data input(normally pull low)	33	VSYNC	Vertical sync input in parallel RGB. Negative polarity	
enable data input(normally pull low)	3/		Input data enable control. When DE mode, active High to	
	Y 04		enable data input(normally pull low)	
	35	NC	No connection	
36 GND Power ground		GND		
37 XR(NC) Right electrode-differential analog	37	XR(NC)		
38 YD(NC) Bottom electrode-differential analog	38	YD(NC)		
39 XL(NC) Left electrode-differential analog				
40 YU(NC) Top electrode-differential analog	40	YU(NC)	Top electrode-differential analog	

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3.2 TP Input/Output Terminals

Pin NO.	Logic	Remark
1	YU	
2	XL	
3	YD	
4	XR	

4 Electrical Characteristics

4.1 Driving TFT LCD Panel

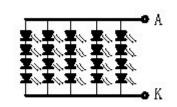
ltem	Symbol	Min	Тур.	Max	Unit	Remark
Digital Power Voltage	VDD	3.0	3.3	3.6	V	
Gate on Voltage	VGH	-	15	-	V	X
Gate off Voltage	VGL	-	-10	-	V	
Common Voltage	VCOM	-	0	-	V	

4.2 LED Backlight Specification

ltem	Symbol	Min.	Тур.	Max.	Unit	Remark
Forward Voltage	VF	11	12	13	V	
Forward Current	lF	-	100		mA	
Luminance	Lv	-	600	-	cd/m ²	lf=100mA
Uniformity(with L/G)	YU	75	80	-	%	
LED Life-Time	Hr		30000	-	Hour	

Note: 20 LEDs (4LEDs Serial,5ways Parallel)





Circuit Diagram

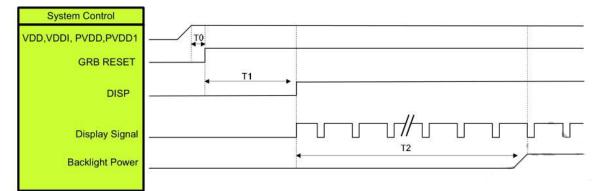
5 Timing Characteristics

5.1 HV Mode

			Value		
Parameter	Symbol	Min.	Тур.	Max.	Unit
Horizontal Display Area	thd		800		DCLK
DLCK Frequency	Fclk	26.4	33.3	46.1	MHZ
HSD Period	Th	862	1056	1200	DCLK
HSD Pulse Width	thpw	1	36	40	DCLK
HSD Back Porch	thbp		48		DCLK
HSD Front Porch	tfbp	16	210	354	DCLK
Vertical Display Area	tvd		480	V	Н
VSD period	tv	510	525	650	Н
VSD Pulse Width	Тvpw	1	4	20	Н
VSD Back Porch	Tvbp	\sim	12		Н
VSD Front Porch	tvfp	7	22	147	Н

5.2 Up-down Time Sequence

5.2.1 On

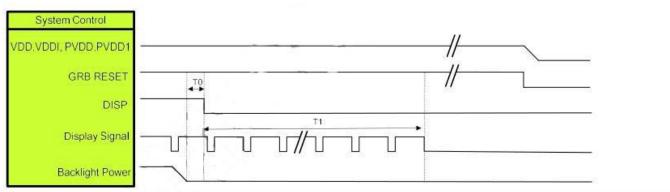


Symbol	Description	Min. Time	Unit
Т0	System power stability to GRB RESET signal	0	ms
T1	GRB RESET= "High" to DISP="High"	10	ms
T2	Display Signal output to Backlight Power on	250	ms

Note: RGB interface Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]

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5.2.2 Off

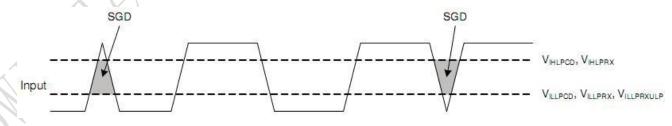


Symbol	Description	Min. Time	Unit
Т0	Backlight Power off to DISP="Low"	5	ms
T1	DISP="Low" to IC internal voltage discharge complete	100	ms

Note: RGB interface Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]

5.3 Mode DC Electrical Characteristics

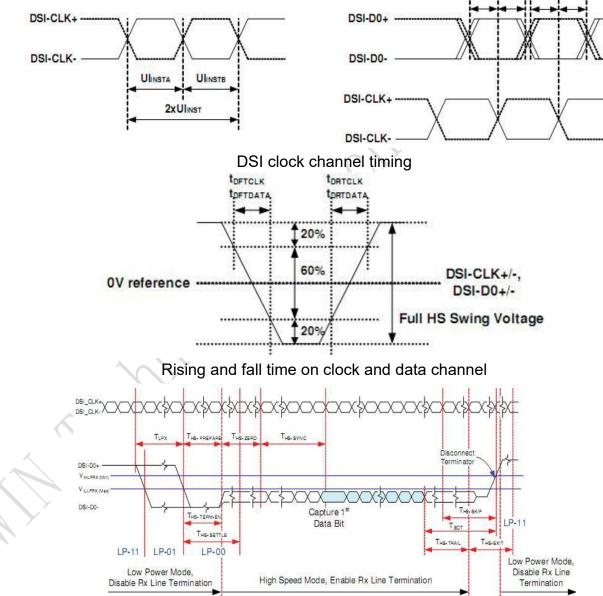
Developmentary	Cumhal	Oraditions	S	pecificatio	n	LINUT
Parameter	Symbol	Conditions	MIN	TYP	MAX	UNIT
Logic high level input voltage	VIHLPCD	LP-CD	450		1350	mV
Logic low level input voltage	VILLPCD	LP-CD	0	*	200	mV
Logic high level input voltage	VIHLPRX	LP-RX (CLK, D0, D1)	880	*	1350	mV
Logic low level input voltage	VILLPRX	LP-RX (CLK, D0, D1)	0	×	550	mV
Logic low level input voltage	VILLPRXULP	LP-RX (CLK ULP mode)	0		300	mV
Logic high level output voltage	Vohlptx	LP-TX (D0)	1.1		1.3	V
Logic low level output voltage	VOLLPTX	LP-TX (D0)	-50		50	mV
Logic high level input current	<mark>Ін</mark>	LP-CD, LP-RX	2 2011	20 20	10	μA
Logic low level input current	lı.	LP-CD, LP-RX	-10	2	928) 1	μA
Input pulse rejection	SGD	DSI-CLK+/-, DSI-Dn+/- (Note 3)	24 C		300	Vps



Spike/glitch rejection-DSI

5.4 AC Characteristics

gnal	Symbol	Parameter	MIN	TYP	MAX	Unit	Description
			4		8	ns	4 Lane (Note 2)
CLK+/-	2xUIINST	Double UI instantaneous	3	100	8	ns	3 Lane (Note 2)
		2.352	8	8	ns	2 Lane (Note 3)	
	Library	Ul instantaneous halfs	2	370	4	ns	4 Lane (Note 2)
DSI-CLK+/-	UIINSTA UINSTB		1.5	-	4	ns	3 Lane (Note 2)
		(UI = UIINSTA = UIINSTB)	1.176	8 9 0	4	ns	2 Lane (Note 3)
-Dn+/-	tos	Data to clock setup time	0.15xUI	571	5	ps	
-Dn+/-	toн	Data to clock hold time	0.15xUI	-	-	ps	
CLK+/-	TDRTCLK	Differential rise time for clock	150	(in)	0.3xUI	ps	
-Dn+/-	t drtdata	Differential rise time for data	150	571	0.3xUI	ps	
CLK+/-	TDFTCLK	Differential fall time for clock	150	5 (0.3xUI	ps	5
-Dn+/-	t dftdata	Differential fall time for data	150		0.3xUI	ps	

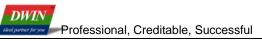


6 Optical Characteristics

ltem	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	Тор		70	80	-		
	Bottom	CR≧10	70	80	-	Dec	Note 2
Viewing Angle	Left	CR≦ 10	70	80	-	Deg.	Note 2
	Right		70	80	-		
Contrast Ratio	CR	θ=0°	800	1000	-		
Response Time	T _r +T _f	θ=0°	-	35	50	ms	
	Wx		0.260	0.290	0.320		
	Wy		0.290	0.320	0.350		
	Rx		\sim	\mathcal{O}_{λ}	-		
Color Chromaticity	Ry		00	× _	-		Note 1
(CIE1931)	Gx	θ=0°	-	-	-		Note 1
	Gy	00	-	-	-		
	Вх		-	-	-		
	Ву		-	-	-		
Color Gamut	NTSC	θ=0°	-	55	-	%	

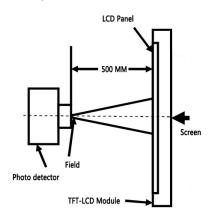
Test conditions:

IF= 100 mA, and the ambient temperature is 25° C.

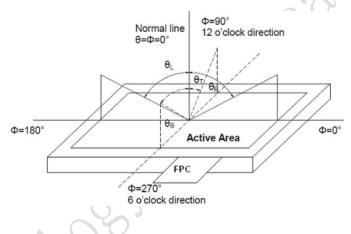


Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of LCD.



Note 2: Definition of viewing angle range and measurement system. The viewing angle is measured at the center point of the LCD by BM-7A.



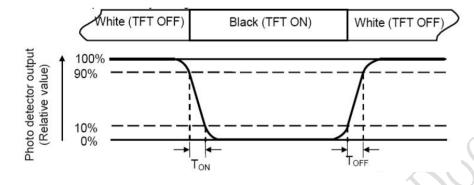
Note 3: Definition of color temperature.

When the radiation of the light source is exactly the same in the visible region and the absolute blackbody, the temperature of the blackbody is called the color temperature of the light source. Color temperature is an index to measure the degree of light source color (cold color, warm color). Warm color < 3300K, intermediate color 3300 ~ 5000K, cold color > 5000K.

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Note 4: Definition of response time.

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Time ON (TON) is the time between photo detector output intensity changed from 90% to 10%. And time off (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931). Color coordinates measured at center point of LCD.

Note 6: Definition of luminance.

Measure the luminance of white state at center point.

7 Environmental Reliability Test

NO	Test Item	Condition	Remarks	
1	High Temperature Operation	Ta=+60℃,96hours	IEC60068-2-1:2007	
I	Tigh temperature Operation		GB2423.2-2008	
2	Low Temperature Operation	Ta= 0℃,96hours	IEC60068-2-1:2007	
2			GB2423.1-2008	
3	High Temperature Storage	Ta=+70℃,96hours	IEC60068-2-1:2007	
5	Thigh Temperature Storage		GB2423.2-2008	
4	Low Tomporature Storage	Temperature Storage Ta=-10℃,96hours	IEC60068-2-1:2007	
4	Low Temperature Storage		GB2423.1-2008	
5	Vibration Test	10Hz~45Hz,100m/s²,120min	IEC60068-2-6:1982	
5	5 Vibration lest		GB/T 2423.10-1995	
6	Package Drop Test	800mm, concrete floor,1corner, 3edges, 6	IEC60068-2-32:1990	
	Fackage Drop Test	sides each time	GB/T 2423.8-1995	
7	Shock Test	Half-sine wave,300m/s ² ,11ms	IEC60068-2-27:1987	
	SHOCK Test		GB/T 2423.5-1995	
8	ESD(non-operation)	150pF,330Ω,Contact: \pm 4KV,Air: \pm 8KV	IEC61000-4-2:2001	
0	ESD(non-operation)	Measure point: LCD glass and metal bezel	GB/T 17626.2-2006	

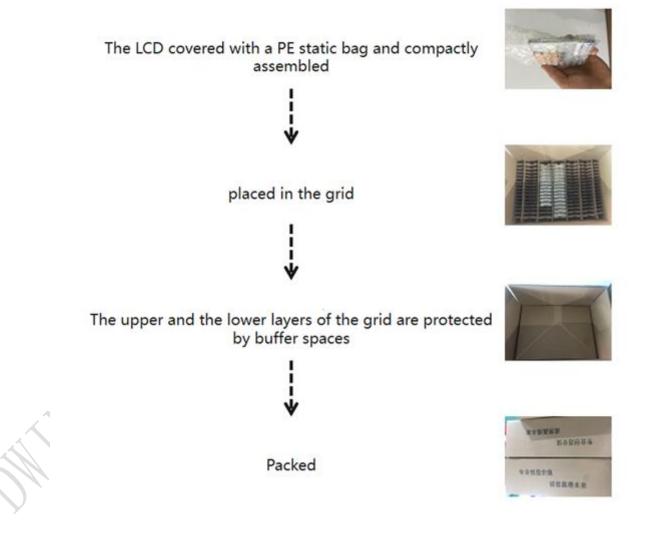
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8 Packing Capacity & Dimension

Dimension				
Dimension(mm)	120.7(W)*75.8(H)*4.3(D)			
Net Weight	-			
Packing Capacity				
Size	LCD Size and Resolution	Layer	Quantity (Pcs)	
220mm(L)x160mm(W)x47mm(H)	5.0 inch 800*480	1	1	
435mm(L)x420mm(W)x290mm(H)	5.0 inch 800*480	1	120	

Packing instruction:

The LCD+TP is placed in the grid, covered with a PE static bag and compactly assembled, the upper and the lower layers of the grid are protected by buffer spaces.



9 Appearance Inspection

9.1 General rules for inspection

9.1.1 Anti-static wearables (anti-static wristbands, gloves) must be worn during the inspection.

9.1.2 Do not use bare hands to touch the position of the device, golden fingers, and the surface of the screen to prevent the sweat from human hands from causing oxidation and affecting the appearance.

9.1.3 It is forbidden to stack products out of specification and handle them with care to avoid damage to components.

9.1.4 The repaired products need to be inspected to prevent rosin and tin slag from exceeding the specifications.

9.1.5 When technical documents and process documents have specific requirements for products, the technical documents and process documents shall be the main requirements.

9.2 Inspection conditions

9.2.1 The conditions of display function check

Angle: ±5°;

Inspection method: visual inspection. The inspection object is 30-40cm away from the light source, and the eye is 30-40cm away from the inspection object;

Illumination: 300-500Lux;

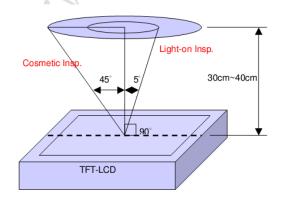
Inspection time: 5-10S.

9.2.2 Visual inspection conditions

Angle: ±45°;

Inspection method: visual inspection. The inspection object is 30-40cm away from the light source, and the eye is 30-40cm away from the inspection object;

Illumination: 800-1500Lux; Inspection time: 5-10S.



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9.3 Inspection standard

Туре	Test Items	Judgement Standard	Defect Category
Display state	Dead pixels	No dead pixels	
	mura	From different angles, the brightness is required to be uniform. Under the 64-level grayscale or pure black interface, there should be no uneven display brightness within the viewing angle range of 45° through 6% ND FILTER. Y series (TV film) LCD screen does not have specific requirements, and the picture inspection does not affect the display as qualified.	Slight defect
		Uneven brightness Black and white mottled	
	Light leakage	Under the 64-level grayscale or pure black interface, there should be no obvious light leakage within the viewing angle range of 45° by visual inspection or through 6% ND FILTER. Y series (TV LCD screen) series can be without obvious visual defects.	
	Linear foreign bodies	 1. W≤0.05, L≤2mm, negligible; 2. 0.05mm<w≤0.1mm, li="" l≤2mm,="" n≤3;<=""> 3. W>0.1mm, L>2mm, not allowed. </w≤0.1mm,>	Slight defect
	Bubble in OCA	1. D<0.20mm, negligible;	Slight defect
	Within the effective area	Spotted: 1. D≤0.2mm and it is not a piece, it is not counted; 2. 0.2mm <d≤0.5mm, n≤3;<="" td=""> 3. D>0.5mm, L>0.5mm, W>0.5mm are not allowed; (The spotted foreign objects shall not exceed the point-line gauge D=0.5, and the black dot coverage shall be checked, and the spotted foreign objects shall be judged within the range of D=0.5) Linear:</d≤0.5mm,>	Slight defect
Screen surface		1. W≤0.05, L≤2mm, ignored; 2. 0.05 <w≤0.1mm, l≤2mm,="" n≤3;<br="">3. W>0.1mm, L>2mm, not allowed.</w≤0.1mm,>	
	Outside the effective area Foreign objects Scratches Air bubbles	Foreign objects are not checked, and bubbles are not allowed to D>1mm; Non-inductive scratches of no more than 0.1×8 mm are allowed.	Slight defect

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LI80480C050HA9098-TR_datasheet Product Specification

			pecilication
	Crack	Not allowed.	Slight defect
	Notch	1. Does not affect the appearance from the front;2. Does not affect the relevant alignment;3. X \leq 1mm, Y \leq 1mm, N \leq 2.	Slight defect
	Glass side Foreign objects Dirty	 The foreign body on the side is not controlled; The paint pen marks on the side are not controlled; Side oily note printing is not allowed. 	Slight defect
	Cracks Goldfinger crease	Not allowed.	Heavy deficit
	Crease	Slight creases are not controlled; The crease is whitish and has lines, which is not allowed.	Heavy deficit
	Top wound,	No damage to the line, D≤0.2mm;	Heavy
500	stab wound	Damage to the line is not allowed.	deficit
FPC -	Quantah	Slight scratches on the surface are not controlled;	Heavy
	Scratch	Damage to the line is not allowed.	
	Goldfinger scratch	$W \le 0.05$ mm, no control; W>0.05mm, not allowed; Test probe tip marks are not controlled.	Heavy deficit
	Component	Under-soldering, over-soldering and false soldering are not allowed.	Heavy deficit

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10 Precautions for Use of LCD Modules

10.1 Handling Precautions

10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, Can only use LCD dedicated cleaner, the following organic solvent can not be used:

Isopropyl alcohol

- Ethyl alcohol
- Ketone
- Aromatic solvents

10.1.6 Do not attempt to disassemble the LCD Module.

10.1.7 If the logic circuit power is off, do not apply the input signals.

10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an 10.1.9 optimum work environment.

10.1.9.1 Be sure to ground the body when handling the LCD Modules.

10.1.9.2 Tools required for assembly, such as soldering irons, must be properly ground.

10.1.9.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

10.1.9.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature: $0^{\circ}C \sim 40^{\circ}C$ Relatively humidity: $\leq 80\%$.

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas. 10.3 Transportation Precautions

10.3.1 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

11 Laminated Screen Introduction

11.1 Laminated screen classification

The laminated screen is mainly composed of cover glass, TP and LCD. The lamination methods can be either frame lamination or full lamination. The frame lamination process fixes TP with the four sides of LCD by 3M adhesive, which is one of the most common lamination methods. Full lamination is to seamlessly bond LCD and TP by optical adhesive. Compared to frame lamination, full lamination features by moisture-proof, dust-proof, high stability, high quality display, and can achieve the visible display under strong light.

11.2 ODM service

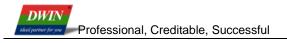
DWIN technology has built the Huan DWIN Science Park with a construction area of 250000 square meters (In addition, another 148000 square meters are under construction), integrating industrial chain of LCM, SMT, CTP, RTP, mold injection, and Sheet metal punching. DWIN can guarantee the production of LCM, CTP and RTP with first-class technology, highly automated and intelligent manufacturing equipment.

The production capacity of LCM lines is 2.5 million. The LCM lines support the production of LCM with high luminance(1200 nit), wide operating temperature(-40~85°C), anti-electromagnetic interference, sunlight readability and HDMI interface.

The production capacity of RTP lines is 5 hundred thousand. The RTP lines support the production of customized 4-wire RTP and 5-wire RTP, anti-UV material and AG material.

The production capacity of CTP lines is 1 million. The CTP lines support the production of customized CTP, including 1.3~21.5 inches (unconventional size), circular CTP, the shape, color and logo of cover plate, anti-UV, anti- fingerprint and AG material. They can also support the customization of various kinds of technologies, such as OCA lamination, ultrathin GFF, optical bonding, 2.5D and sunlight readability.

HCr.





CTP lines

RTP lines

Laboratories

SMT lines

Record of Revision

Rev	Date	Description	Editor
00	2022-12-13	First Release	Chen
01	2023-01-11	Revise Model No. Update Operating Temp and Storage Temp	Chen
02	2023-06-05	Update Luminance	Chen

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Thank you all for continuous support of DWIN, and your approval is the driving force of our progress!